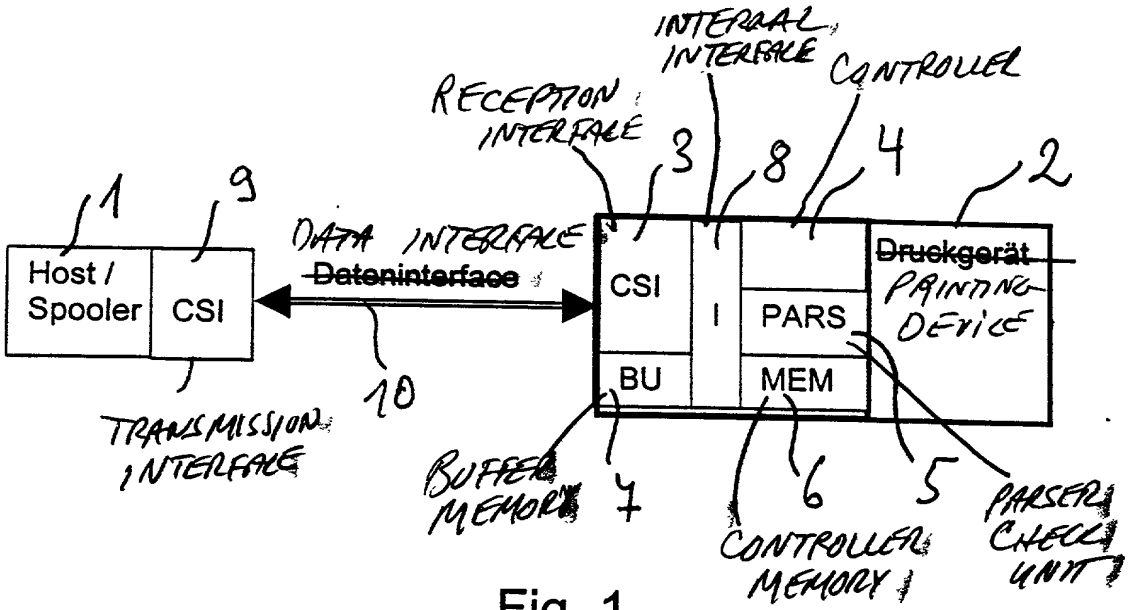
INTERRUPT INTERFACE
CONTROLLER
RECEPTION INTERFACE
3
8
4
2
1
9
DATA INTERFACE
~~Dateninterface~~
Host / Spooler
CSI
CSI
I
PARS
~~Druckgerät~~
PRINTING DEVICE
BU
MEM
10
TRANSMISSION INTERFACE
BUFFER MEMORY
7
6
5
CONTROLLER MEMORY
PARSER CHECK UNIT

Fig. 1

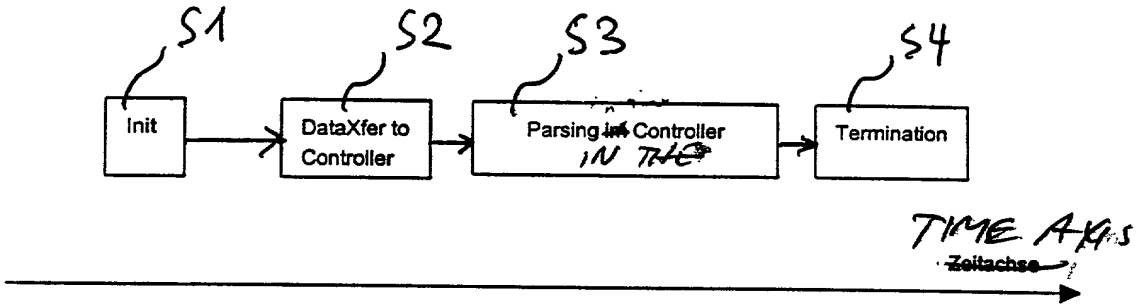
S1
S2
S3
S4
Init
DataXfer to Controller
Parsing ~~in~~ Controller IN THE
Termination
TIME AXIS
~~Zeitachse~~

Fig. 2

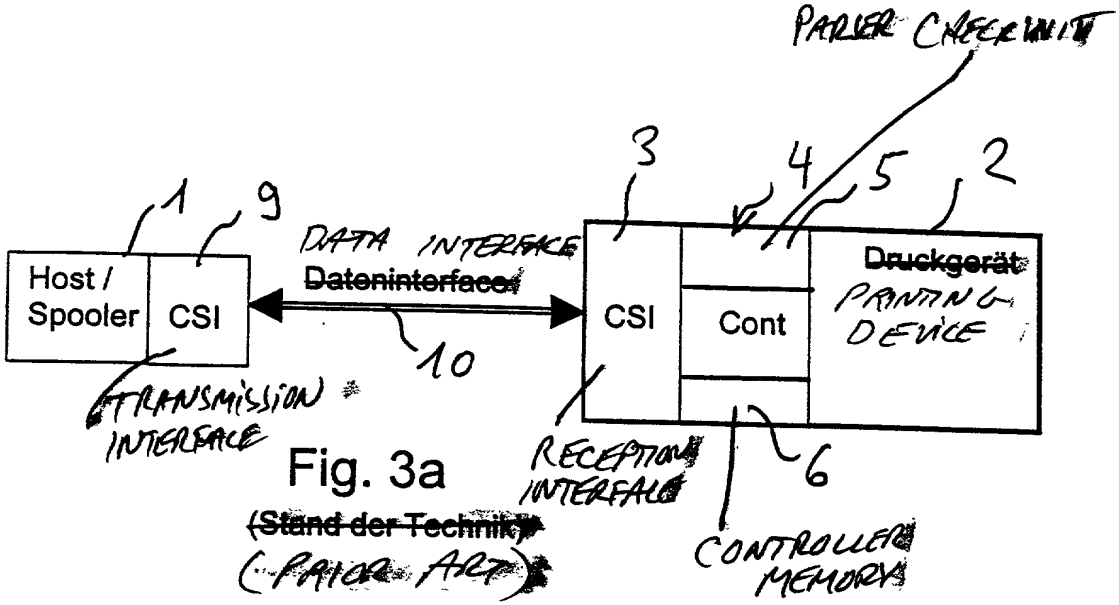

Fig. 3a

(Stand der Technik)

(PRIOR ART)

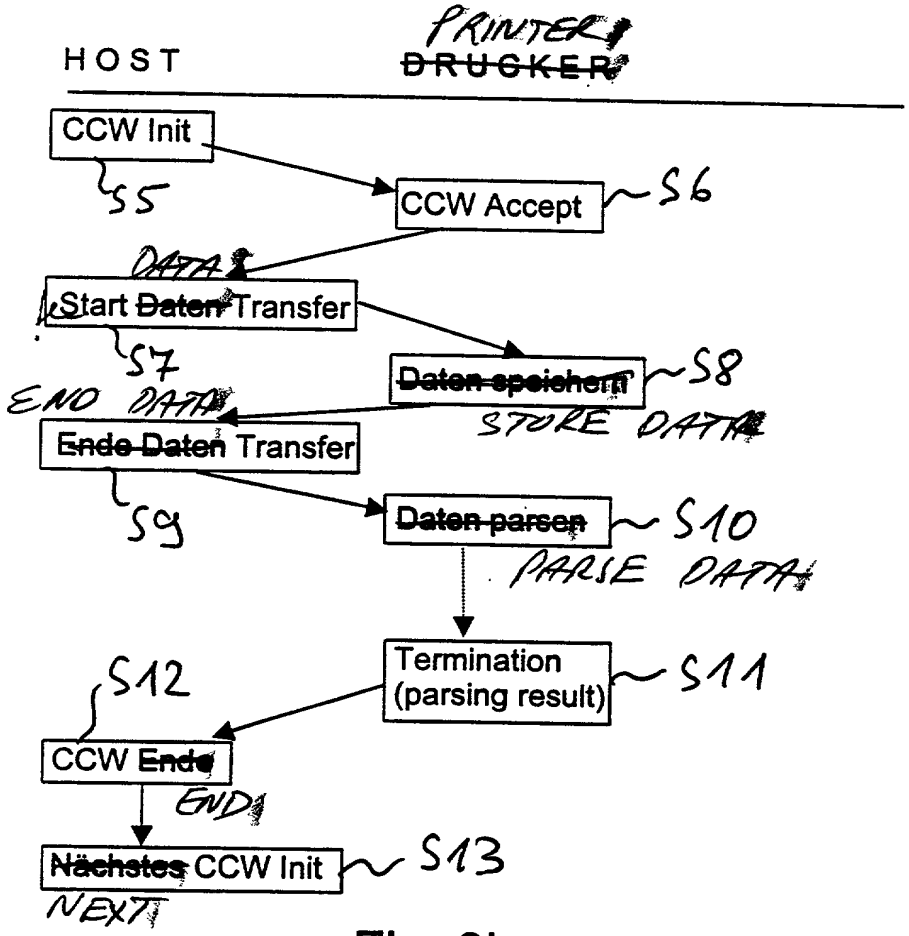

Fig. 3b

(Stand der Technik)

(PRIOR ART)

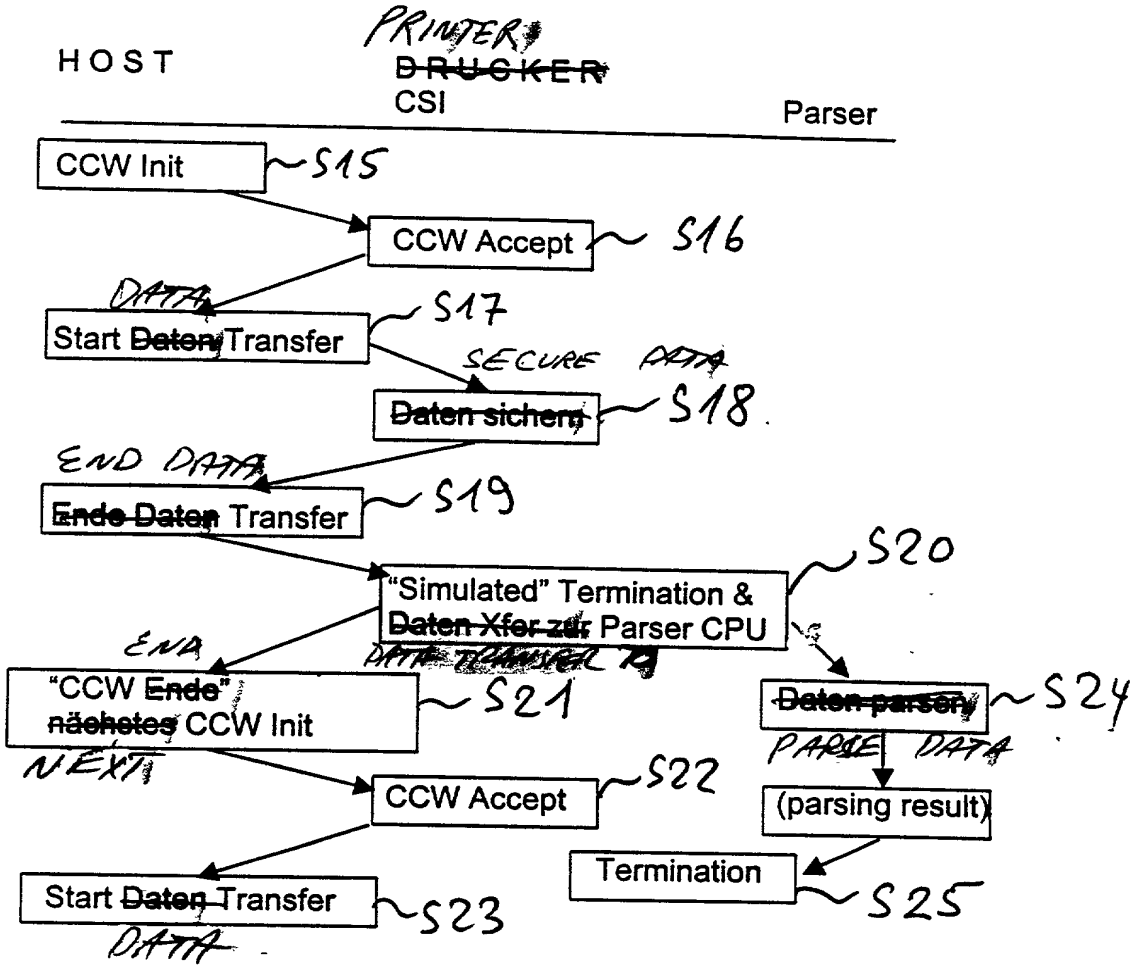
HOST
PRINTER
DRUCKER
CSI
Parser
CCW Init
S15
CCW Accept
S16
DATA
Start Daten Transfer
S17
SECURE DATA
Daten sichern
S18
END DATA
Ende Daten Transfer
S19
"Simulated" Termination &
Daten Xfer zur Parser CPU
S20
ENA
"CCW Ende"
nächstes CCW Init
S21
NEXT
CCW Accept
S22
Start Daten Transfer
DATA
S23
Daten parsen
S24
PARSE DATA
(parsing result)
Termination
S25

Fig. 4

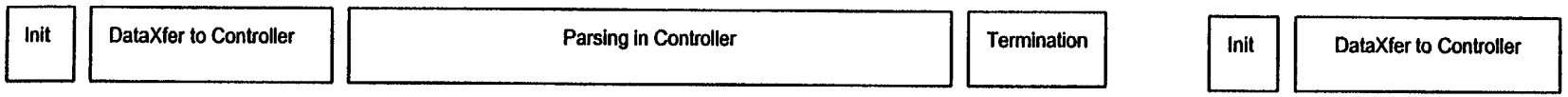

Fig. 5

~~(Stand der Technik)~~
(PRIOR ART)

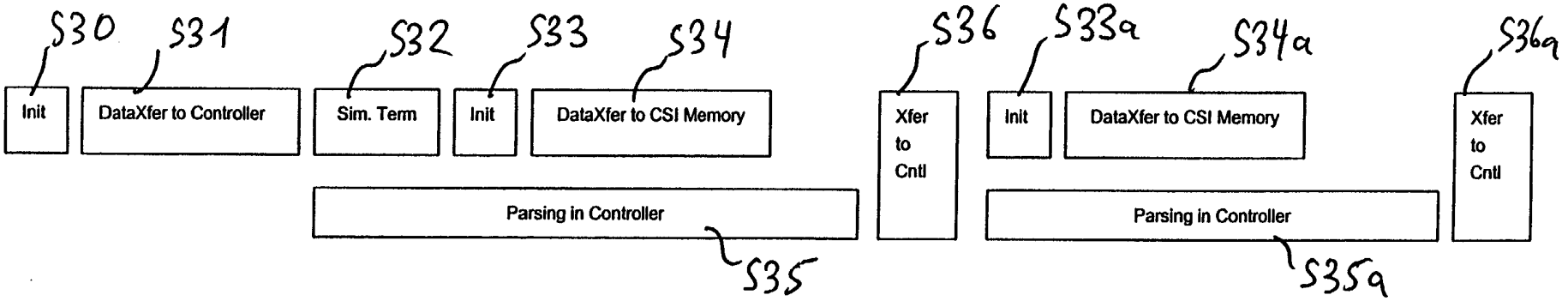

Fig. 6